

HARINGHATA MAHAVIDYALAYA
SEM-II 1st Internal Assessment Examination- 2019
B.SC. (Programme)
Subject- CC-1B
Subject Title- Computer System Architecture
Coverage: Unit-1 to Unit- 2
Submission from 08.03.2019 to 14.03.2019

Answer any two(2) questions

Maximum Marks: 10

1. Write the methods to convert a decimal number to its equivalent binary number with a suitable example.
2. Write a short note on fixed point representation.
3. Discuss full adder and full subtractor.
4. What is decoder? Design a 3X8 decoder.
5. State and explain De Morgans law.
6. Design a 2 bit magnitude comparator.