

HARINGHATA MAHAVIDYALAYA
SEM-II 1st Internal Assessment Examination- 2019
B.SC. (Hons. General Elective)
Subject- GE-2
Subject Title- Computer System Architecture
Coverage: Unit-1 to Unit- 2
Submission from 08.03.2019 to 14.03.2019

Answer any two(2) questions

Maximum Marks: 10

1. State and explain De Morgans law.
2. Write the methods to convert a binary number to its equivalent decimal number with a suitable example.
3. Implement a full adder using two half adder and an OR gate.
4. What is MUX? Design a 1X8 MUX.
5. Write a short note on fixed point representation.
6. Design a 4 bit binary to grey convertor.